

SK35DGDL12T7ETE2



SEMITOP®E2

3-phase
Converter-Inverter-Brake
(CIB)

SK35DGDL12T7ETE2

Features*

- Optimized design for superior thermal performance
- Low inductive design
- Press-Fit contact technology
- 1200V Generation 7 IGBT (T7)
- Robust and soft switching CAL4F diode technology
- PEP rectifier diode technology for enhanced power and environmental robustness
- Integrated NTC temperature sensor
- UL recognized file no. E 63 532

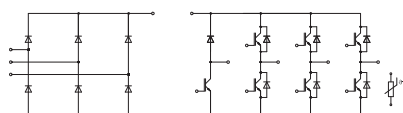
Typical Applications

- Motor drives
- Air conditioning
- Auxiliary Inverters

Remarks

- Recommended $T_{j,op} = -40 \dots +150 \text{ }^{\circ}\text{C}$
- $T_{j,op} > 150 \text{ }^{\circ}\text{C}$ during overload (details on AN19-002)

Absolute Maximum Ratings				
Symbol	Conditions		Values	Unit
Inverter - IGBT				
V _{CES}	T _j = 25 °C		1200	V
I _C	λ _{paste} =0.8 W/(mK)	T _s = 70 °C	41	A
	T _j = 175 °C	T _s = 100 °C	34	A
I _C	λ _{paste} =2.5 W/(mK)	T _s = 70 °C	50	A
	T _j = 175 °C	T _s = 100 °C	41	A
I _{Cnom}			35	A
I _{CRM}			70	A
V _{GES}			-20 ... 20	V
t _{psc}	V _{CC} = 800 V V _{GE} ≤ 15 V V _{CES} ≤ 1200 V	T _j = 175 °C	7	μs
T _j			-40 ... 175	°C
Chopper - IGBT				
V _{CES}	T _j = 25 °C		1200	V
I _C	λ _{paste} =0.8 W/(mK)	T _s = 70 °C	41	A
	T _j = 175 °C	T _s = 100 °C	34	A
I _C	λ _{paste} =2.5 W/(mK)	T _s = 70 °C	50	A
	T _j = 175 °C	T _s = 100 °C	41	A
I _{Cnom}			35	A
I _{CRM}			70	A
V _{GES}			-20 ... 20	V
t _{psc}	V _{CC} = 800 V V _{GE} ≤ 15 V V _{CES} ≤ 1200 V	T _j = 175 °C	7	μs
T _j			-40 ... 175	°C
Inverse - Diode				
V _{RRM}	T _j = 25 °C		1200	V
I _F	λ _{paste} =0.8 W/(mK)	T _s = 70 °C	33	A
	T _j = 175 °C	T _s = 100 °C	27	A
I _F	λ _{paste} =2.5 W/(mK)	T _s = 70 °C	39	A
	T _j = 175 °C	T _s = 100 °C	32	A
I _{FRM}			70	A
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 150 °C		170	A
T _j			-40 ... 175	°C
Freewheeling - Diode				
V _{RRM}	T _j = 25 °C		1200	V
I _F	λ _{paste} =0.8 W/(mK)	T _s = 70 °C	12	A
	T _j = 175 °C	T _s = 100 °C	10	A
I _F	λ _{paste} =2.5 W/(mK)	T _s = 70 °C	13	A
	T _j = 175 °C	T _s = 100 °C	11	A
I _{FRM}			20	A
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 150 °C		36	A
T _j			-40 ... 175	°C



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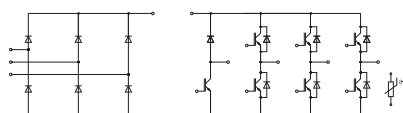
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- $T_{j,op} > 150 \text{ °C}$ during overload (details on AN19-002)

Absolute Maximum Ratings

Symbol	Conditions	Values	Unit
Rectifier - Diode			
V_{RRM}	$T_j = 25 \text{ °C}$	1600	V
I_F	$\lambda_{paste}=0.8 \text{ W/(mK)}$	$T_s = 70 \text{ °C}$	A
	$T_j = 175 \text{ °C}$	$T_s = 100 \text{ °C}$	A
I_F	$\lambda_{paste}=2.5 \text{ W/(mK)}$	$T_s = 70 \text{ °C}$	A
	$T_j = 175 \text{ °C}$	$T_s = 100 \text{ °C}$	A
I_{FSM}	$t_p = 10 \text{ ms}$	$T_j = 25 \text{ °C}$	A
	$\sin 180^\circ$	$T_j = 150 \text{ °C}$	A
i^2t	$t_p = 10 \text{ ms}$	$T_j = 25 \text{ °C}$	A ² s
	$\sin 180^\circ$	$T_j = 150 \text{ °C}$	A ² s
T_j		-40 ... 175	°C
Module			
$I_{t(RMS)}$	$\Delta T_{terminal}$ at PCB joint = 30 K, per pin	30	A
T_{stg}	module without TIM	-40 ... 125	°C
V_{isol}	AC, sinusoidal, 1 min	2500	V

Characteristics

Symbol	Conditions		min.	typ.	max.	Unit
Inverter - IGBT						
V _{CE(sat)}	I _C = 35 A	T _j = 25 °C		1.60	1.75	V
	V _{GE} = 15 V	T _j = 150 °C		1.78	1.93	V
	chiplevel	T _j = 175 °C		1.82	1.97	V
V _{CE0}		T _j = 25 °C		1.00	1.05	V
	chiplevel	T _j = 150 °C		0.80	0.85	V
		T _j = 175 °C		0.75	0.80	V
r _{CE}	V _{GE} = 15 V	T _j = 25 °C		17	20	mΩ
	chiplevel	T _j = 150 °C		28	31	mΩ
		T _j = 175 °C		31	33	mΩ
V _{GE(th)}	V _{GE} = V _{CE} , I _C = 0.75 mA		5.15	5.8	6.45	V
I _{CES}	V _{GE} = 0 V, V _{CE} = 1200 V, T _j = 25 °C				1	mA
C _{ies}	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		6.60		nF
C _{oes}		f = 1 MHz		0.09		nF
C _{res}		f = 1 MHz		0.02		nF
Q _G	V _{GE} = -15 V ... +15 V			487		nC
R _{Gint}	T _j = 25 °C			0		Ω
t _{d(on)}	V _{CC} = 600 V	T _j = 25 °C		43		ns
	I _C = 35 A	T _j = 150 °C		45		ns
t _r	R _{G on} = 5.6 Ω	T _j = 175 °C		46		ns
	R _{G off} = 5.6 Ω	T _j = 25 °C		30		ns
	V _{GE} = +15/-15 V	T _j = 150 °C		35		ns
		(T _j = 150 °C)	T _j = 175 °C		37	
E _{on}	di/dt _{on} = 1160 A/μs	T _j = 25 °C		2.51		mJ
	di/dt _{off} = 620 A/μs	T _j = 150 °C		3.52		mJ
	dv/dt = 4600 V/μs	T _j = 175 °C		3.96		mJ



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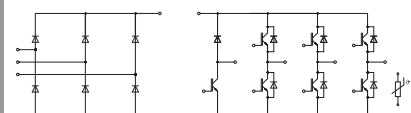
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Remarks

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- $T_{j,op} > 150 \text{ }^{\circ}\text{C}$ during overload (details on AN19-002)



DGDL-ET

Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
Inverter - IGBT						
t _{d(off)}	V _{CC} = 600 V	T _j = 25 °C		183		ns
	I _C = 35 A	T _j = 150 °C		254		ns
	R _{G on} = 5.6 Ω	T _j = 175 °C		274		ns
t _f	R _{G off} = 5.6 Ω	T _j = 25 °C		62		ns
	V _{GE} = +15/-15 V	T _j = 150 °C		95		ns
	(T _j = 150 °C)	T _j = 175 °C		102		ns
E _{off}	di/dt _{on} = 1160 A/μs	T _j = 25 °C		2.83		mJ
	di/dt _{off} = 620 A/μs	T _j = 150 °C		3.74		mJ
	dv/dt = 4600 V/μs	T _j = 175 °C		4.29		mJ
R _{th(j-s)}	per IGBT, λ _{paste} =0.8 W/(mK)			1.17		K/W
R _{th(j-s)}	per IGBT, λ _{paste} =2.5 W/(mK)			0.85		K/W
Chopper - IGBT						
V _{CE(sat)}	I _C = 35 A	T _j = 25 °C		1.60	1.75	V
	V _{GE} = 15 V	T _j = 150 °C		1.78	1.93	V
	chiplevel	T _j = 175 °C		1.82	1.97	V
V _{CE0}		T _j = 25 °C		1.00	1.05	V
	chiplevel	T _j = 150 °C		0.80	0.85	V
		T _j = 175 °C		0.75	0.80	V
r _{CE}	V _{GE} = 15 V	T _j = 25 °C		17	20	mΩ
	chiplevel	T _j = 150 °C		28	31	mΩ
		T _j = 175 °C		31	33	mΩ
V _{GE(th)}	V _{GE} = V _{CE} , I _C = 0.75 mA		5.15	5.8	6.45	V
I _{CES}	V _{GE} = 0 V, V _{CE} = 1200 V, T _j = 25 °C				1	mA
C _{ies}	V _{CE} = 25 V	f = 1 MHz		6.60		nF
C _{oes}	V _{GE} = 0 V	f = 1 MHz		0.09		nF
C _{res}		f = 1 MHz		0.02		nF
Q _G	V _{GE} = -15 V ... +15 V			487		nC
R _{Gint}	T _j = 25 °C			0		Ω
t _{d(on)}		T _j = 25 °C		43		ns
		T _j = 150 °C		45		ns
		T _j = 175 °C		46		ns
t _r		T _j = 25 °C		30		ns
		T _j = 150 °C		35		ns
		T _j = 175 °C		37		ns
E _{on}	V _{CC} = 600 V	T _j = 25 °C		2.51		mJ
	I _C = 35 A	T _j = 150 °C		3.52		mJ
	R _{G on} = 5.6 Ω	T _j = 175 °C		3.96		mJ
	R _{G off} = 5.6 Ω					
	V _{GE} = +15/-15 V					
t _{d(off)}		T _j = 25 °C		183		ns
	(T _j = 150 °C)	T _j = 150 °C		254		ns
	di/dt _{on} = 1160 A/μs	T _j = 175 °C		274		ns
t _f	di/dt _{off} = 620 A/μs	T _j = 25 °C		62		ns
	dv/dt = 4600 V/μs	T _j = 150 °C		95		ns
		T _j = 175 °C		102		ns
E _{off}		T _j = 25 °C		2.83		mJ
		T _j = 150 °C		3.74		mJ
		T _j = 175 °C		4.29		mJ
R _{th(j-s)}	per IGBT, λ _{paste} =0.8 W/(mK)			1.17		K/W
R _{th(j-s)}	per IGBT, λ _{paste} =2.5 W/(mK)			0.85		K/W



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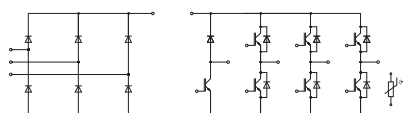
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- Recommended $T_{j,op} = -40 \dots +150 \text{ }^{\circ}\text{C}$
- $T_{j,op} > 150 \text{ }^{\circ}\text{C}$ during overload (details on AN19-002)

Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
Inverse - Diode						
V _F = V _{EC}	I _F = 35 A	T _j = 25 °C		2.30	2.62	V
		T _j = 150 °C		2.29	2.62	V
		chiplevel	T _j = 175 °C		2.14	2.46
V _{F0}	chiplevel	T _j = 25 °C		1.30	1.50	V
		T _j = 150 °C		0.90	1.10	V
		T _j = 175 °C		0.82	0.98	V
r _F	chiplevel	T _j = 25 °C		29	32	mΩ
		T _j = 150 °C		40	43	mΩ
		T _j = 175 °C		38	42	mΩ
I _{RRM}	V _{CC} = 600 V I _F = 35 A V _{GE} = -15 V (T _j = 150 °C) di/dt _{off} = 1030 A/μs	T _j = 25 °C		25		A
		T _j = 150 °C		31		A
		T _j = 175 °C		37		A
Q _{rr}		T _j = 25 °C		2.15		μC
		T _j = 150 °C		4.85		μC
		T _j = 175 °C		5.48		μC
E _{rr}		T _j = 25 °C		1.46		mJ
		T _j = 150 °C		2.39		mJ
		T _j = 175 °C		3.65		mJ
R _{th(j-s)}	per Diode, λ _{paste} =0.8 W/(mK)			1.34		K/W
R _{th(j-s)}	per Diode, λ _{paste} =2.5 W/(mK)			1.01		K/W
Freewheeling - Diode						
V _F = V _{EC}	I _F = 10 A	T _j = 25 °C		2.59	2.94	V
		T _j = 150 °C		2.71	3.08	V
		chiplevel	T _j = 175 °C		2.53	2.89
V _{F0}	chiplevel	T _j = 25 °C		1.30	1.50	V
		T _j = 150 °C		0.90	1.10	V
		T _j = 175 °C		0.82	0.98	V
r _F	chiplevel	T _j = 25 °C		129	144	mΩ
		T _j = 150 °C		181	198	mΩ
		T _j = 175 °C		171	191	mΩ
I _{RRM}	V _{CC} = 600 V I _F = 10 A V _{GE} = -15 V (T _j = 150 °C) di/dt _{off} = 790 A/μs	T _j = 25 °C		8		A
		T _j = 150 °C		14		A
		T _j = 175 °C		16		A
Q _{rr}		T _j = 25 °C		0.58		μC
		T _j = 150 °C		2.01		μC
		T _j = 175 °C		2.37		μC
E _{rr}		T _j = 25 °C		0.36		mJ
		T _j = 150 °C		0.91		mJ
		T _j = 175 °C		1.16		mJ
R _{th(j-s)}	per Diode, λ _{paste} =0.8 W/(mK)			2.64		K/W
R _{th(j-s)}	per Diode, λ _{paste} =2.5 W/(mK)			2.24		K/W



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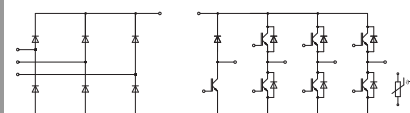
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Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
Rectifier - Diode						
V _F	I _F = 35 A chiplevel	T _j = 25 °C		1.10	1.39	V
		T _j = 150 °C		1.04	1.33	V
		T _j = 175 °C		1.04	1.34	V
V _{F0}	chiplevel	T _j = 25 °C		0.89	1.09	V
		T _j = 150 °C		0.73	0.92	V
		T _j = 175 °C		0.69	0.88	V
r _F	chiplevel	T _j = 25 °C		6.2	8.5	mΩ
		T _j = 150 °C		8.8	12	mΩ
		T _j = 175 °C		10.0	13	mΩ
I _R	T _j = 150 °C, V _{RRM}				2	mA
R _{th(j-s)}	per Diode, λ _{paste} =0.8 W/(mK)			1.48		K/W
R _{th(j-s)}	per Diode, λ _{paste} =2.5 W/(mK)			1.14		K/W
Module						
M _s	to heatsink		1.6		2.3	Nm
w				35		g
L _{CE}				30		nH
Temperature Sensor						
R ₁₀₀	T _C =100°C (R ₂₅ =5 kΩ)			493 ± 5%		Ω
B _{25/85}	R _(T) =R ₂₅ *exp[B _{25/85} *(1/T-1/298)], T[K]			3420		K



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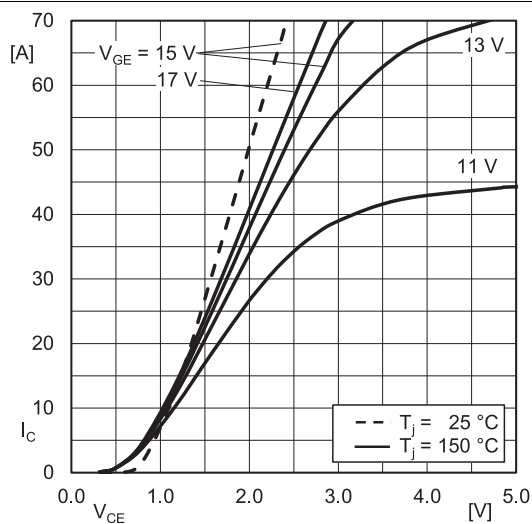


Fig. 1: Typ. IGBT output characteristic, incl. $R_{CC+EE'}$

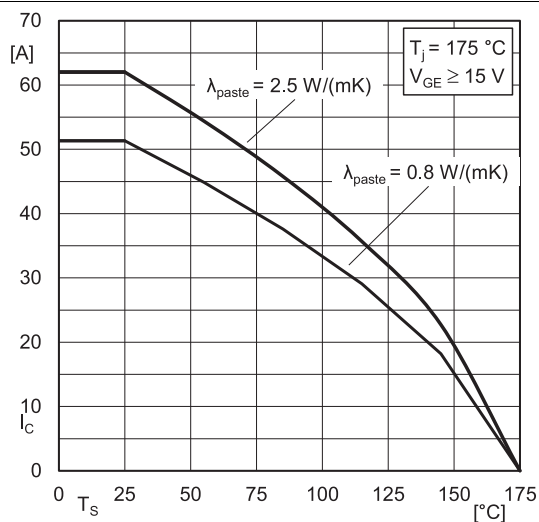


Fig. 2: IGBT rated current vs. temperature $I_C=f(T_s)$

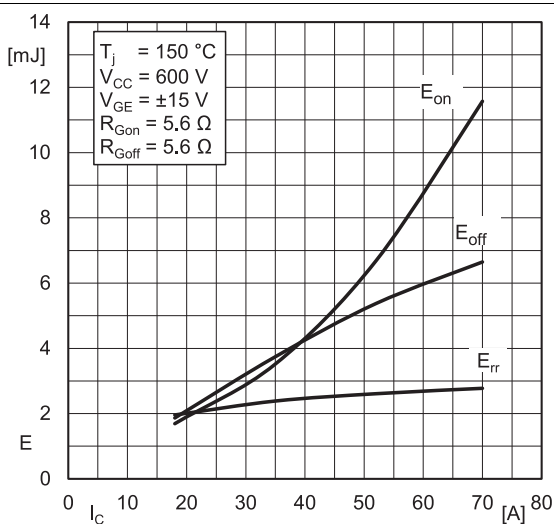


Fig. 3: Typ. turn-on /-off energy = $f(I_C)$

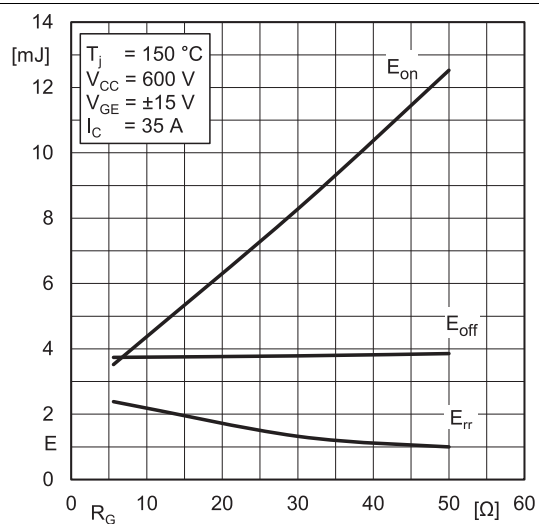


Fig. 4: Typ. turn-on /-off energy = $f(R_G)$

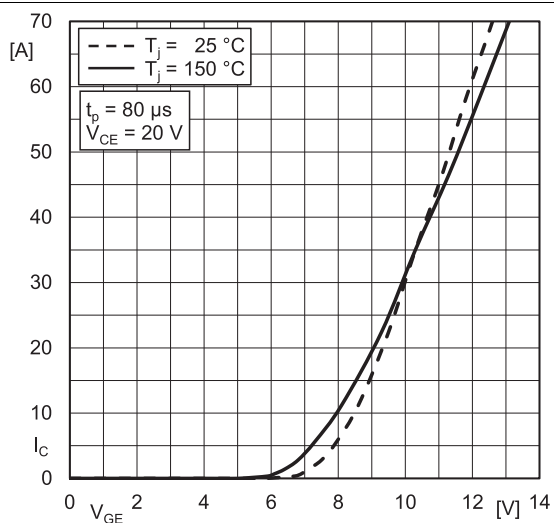


Fig. 5: Typ. IGBT transfer characteristic

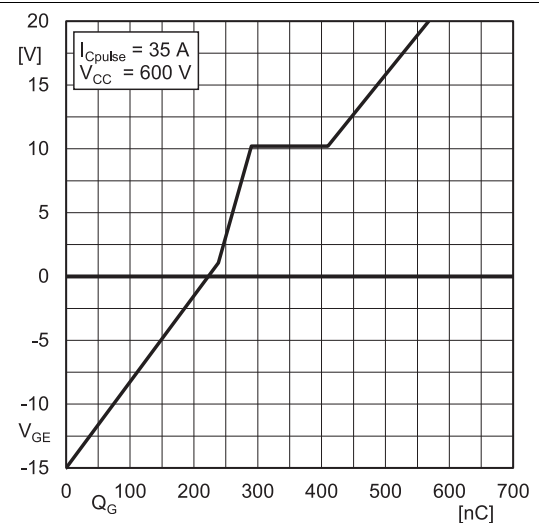


Fig. 6: Typ. IGBT gate charge characteristic

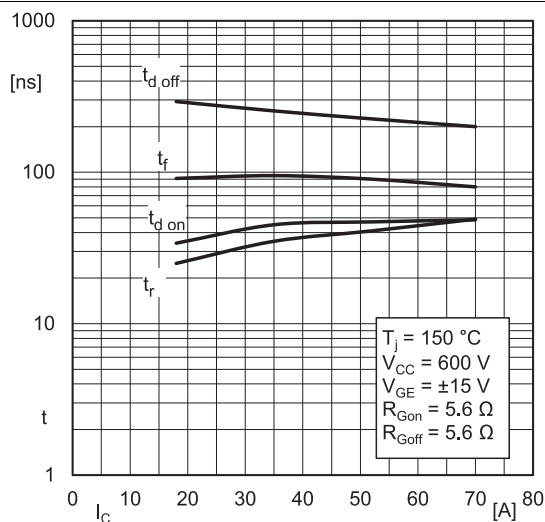


Fig. 7: Typ. switching times = f (I_C)

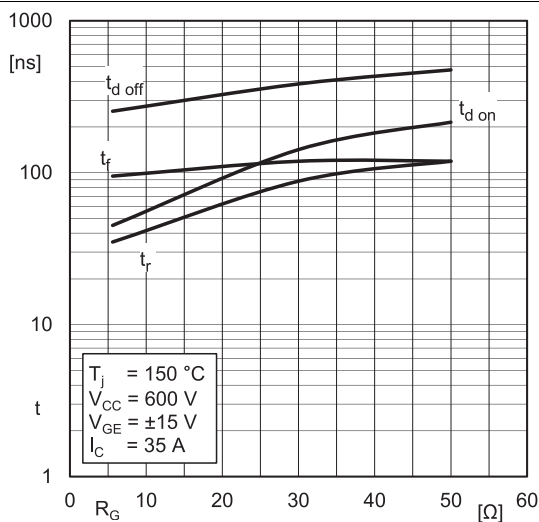


Fig. 8: Typ. switching times = f (R_G)

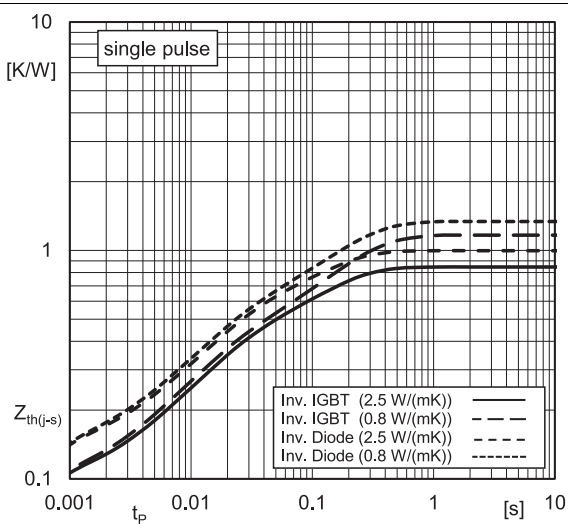


Fig. 9: Typ. transient thermal impedance

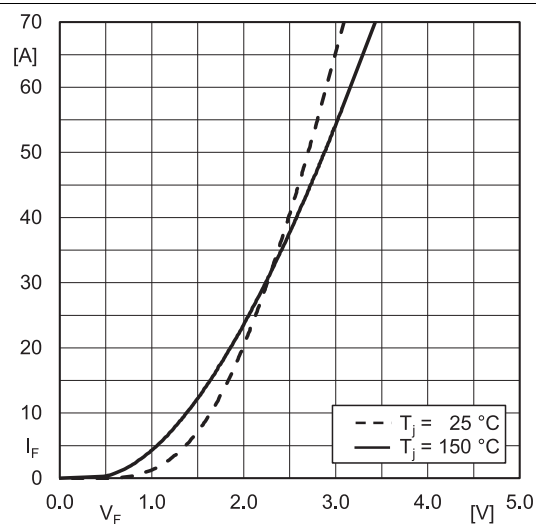


Fig. 10: Typ. Inv. diode forward charact., incl. R_{CC'+EE'}

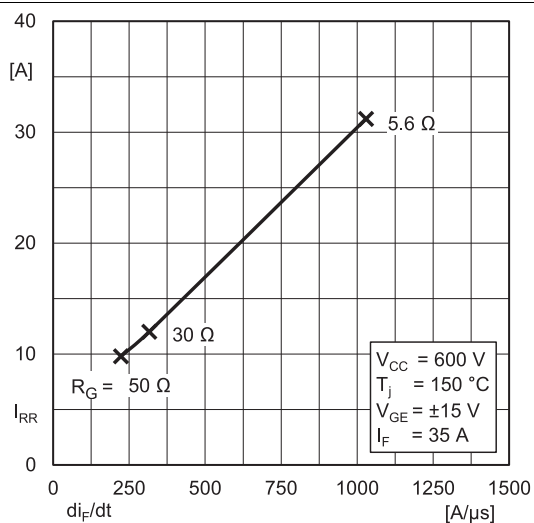


Fig. 11: Typ. Inv. diode peak reverse recovery current

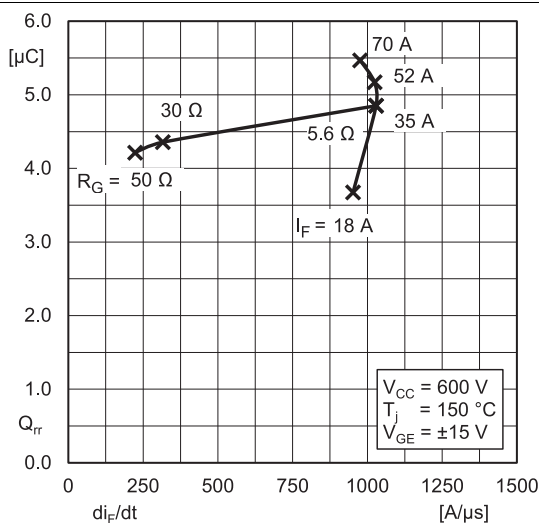


Fig. 12: Typ. Inv. diode reverse recovery charge

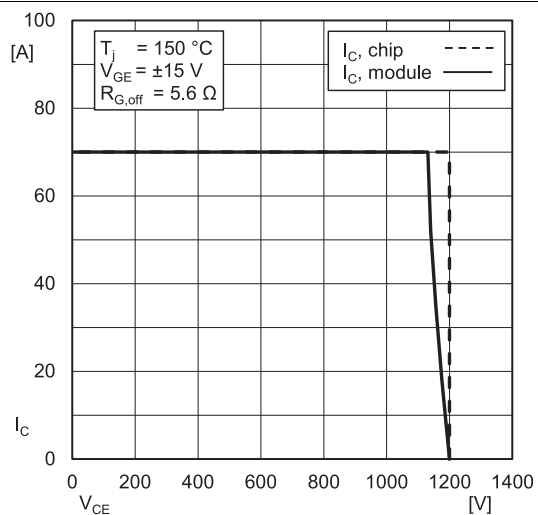


Fig. 13: IGBT Reverse Bias Safe Operating Area (RBSOA)

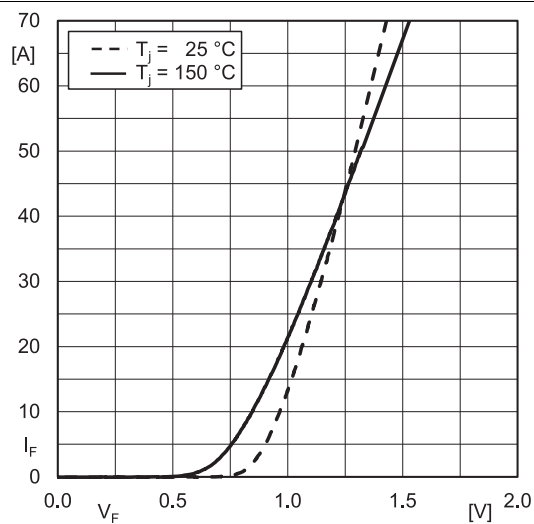
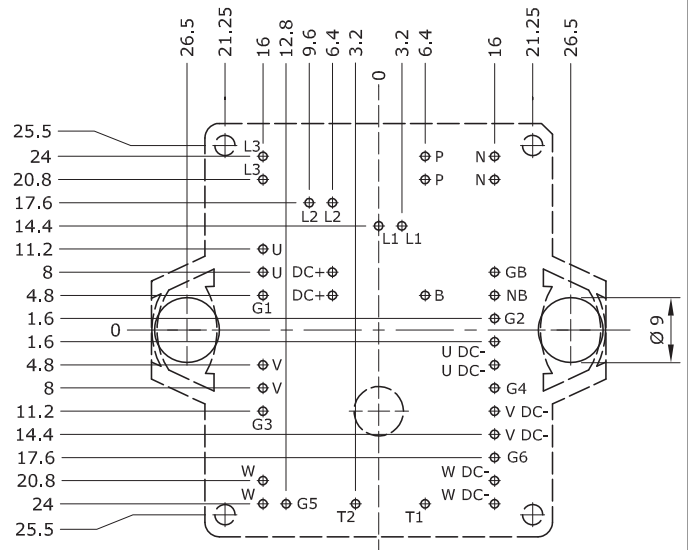
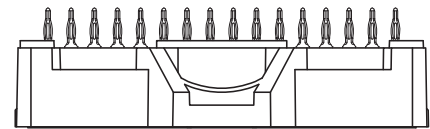
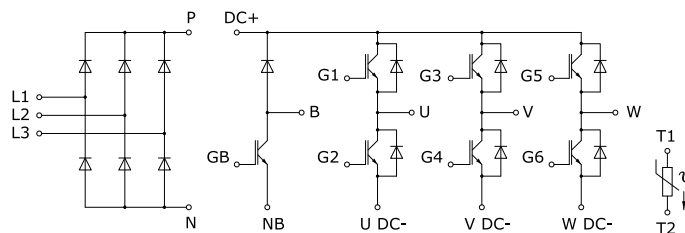


Fig. 14: Typ. Rect. diode forward charact., incl. $R_{CC'+EE'}$



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9

This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

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